

RESUME

## CONTACT INFORMATION

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## OBJECTIVE

To utilize and expand upon an extensive array of knowledge and technical expertise, as well as develop to the fullest potential a generous aptitude for problem-solving, in a position providing a continuous challenging exchange with a talented team of coworkers. The ideal environment for my interests includes close work with other software engineers that are interested in honing their craft.

## EDUCATION

**BA Mathematics**, University of South Florida  
August 2006 Magna Cum Laude  
**BSc Computer Science**, University of South Florida  
December 2001 Magna Cum Laude  
**AA Computer & Information Systems Engineering**, Manatee Community College  
May 1999 Summa Cum Laude

## EMPLOYMENT

**Software Engineer III — JDSU (Formerly Acterna)** *March 2005 — Current*  
*Sarasota, FL*

Designed and implemented key product features. Initiated a Software Control strategy to formalize how developers were keeping track of new development and ensuring the quality of the released software. Initiated the migration to Visual Studio 2005 to ensure the team had the latest technology available. Frequently provided technical guidance on projects, both internal and external to my product group. Presented new technology developments at a company wide engineering conference.

**Senior Software Engineer — Standard Analytics, LLC.** *November 2004 — March 2005*  
*Sarasota, FL*

Implemented and installed upgrades on a production system to do Out-of-Stock detection for retail items. The development environment was awk/sed/php and C on AIX machines.

**Systems Engineer III — Honeywell International, DSES** *September 2003 — November 2004*  
*Clearwater, FL*

Developed custom device drivers and boot loaders for VxWorks systems. Coordinated research efforts with the University of South Florida to continue development of a Selective Triple Modular Redundancy fpga tool. Maintained linux kernel for PPC7450 SBCs, this included applying patches, and managing kernel configurations. Implemented prototype software to achieve NASA TRL-4 for a NASA Technology Development program. The software spanned linux and vxworks platforms. Utilized UML to communicate system designs.

**Software Engineer — Acterna** *May 1999 — September 2003*  
*Sarasota, FL*

Designed, implemented and debugged an MPEG-TS analyzer that supported multiple interfaces, Gigabit Ethernet, QPSK, QAM, and ASI, in a Xilinx Virtex-II FPGA. Worked closely with the hardware engineers on the bring-up of a new PCI peripheral for the MPEG-TS Analyzer. Became familiar with NT device drivers and their performance of DMA and the userspace mapping of hardware registers. Demonstrated proficiency in software troubleshooting and customer feedback troubleshooting with assistance to the test lab and technical support groups. Successfully managed the transfer of information as functional lead engineer for the MPEG-TS Analyzer during consolidation of national offices, effectively assuming the role of product expert. Acquired simultaneous positions of lead software engineer for the MPEG Transport Stream Analyzer and the Return Path Monitoring System for cable networks.

## SKILLS SET

<b>Leadership:</b>	Project lead and management; team skills and cross-functional relations development.
<b>Development:</b>	C; C++; C#; .NET 2.0; Windows Forms 2.0; Java; Verilog; Perl; HTML; PHP; TCP; UDP; IP; SNMP; JNI; sockets; MFC GUI; Oracle PL/SQL; COM; visual debuggers; source code control; make
<b>Operating Systems:</b>	Linux; NetBSD; Solaris; Windows 98/NT/2K/XP; VxWorks
<b>Theory:</b>	Networking (extensive background); Mathematics; Artificial Intelligence; daily reading and research on embedded systems, software engineering best practices, multi-threaded programming, project management, memory management.
<b>Certification:</b>	Sun Certified Programmer for Java 2 Platform (Exam 310-025) 2001

## REFERENCES

Available upon request.